

Fig. 1
(Prior Art)

Patent 0825007

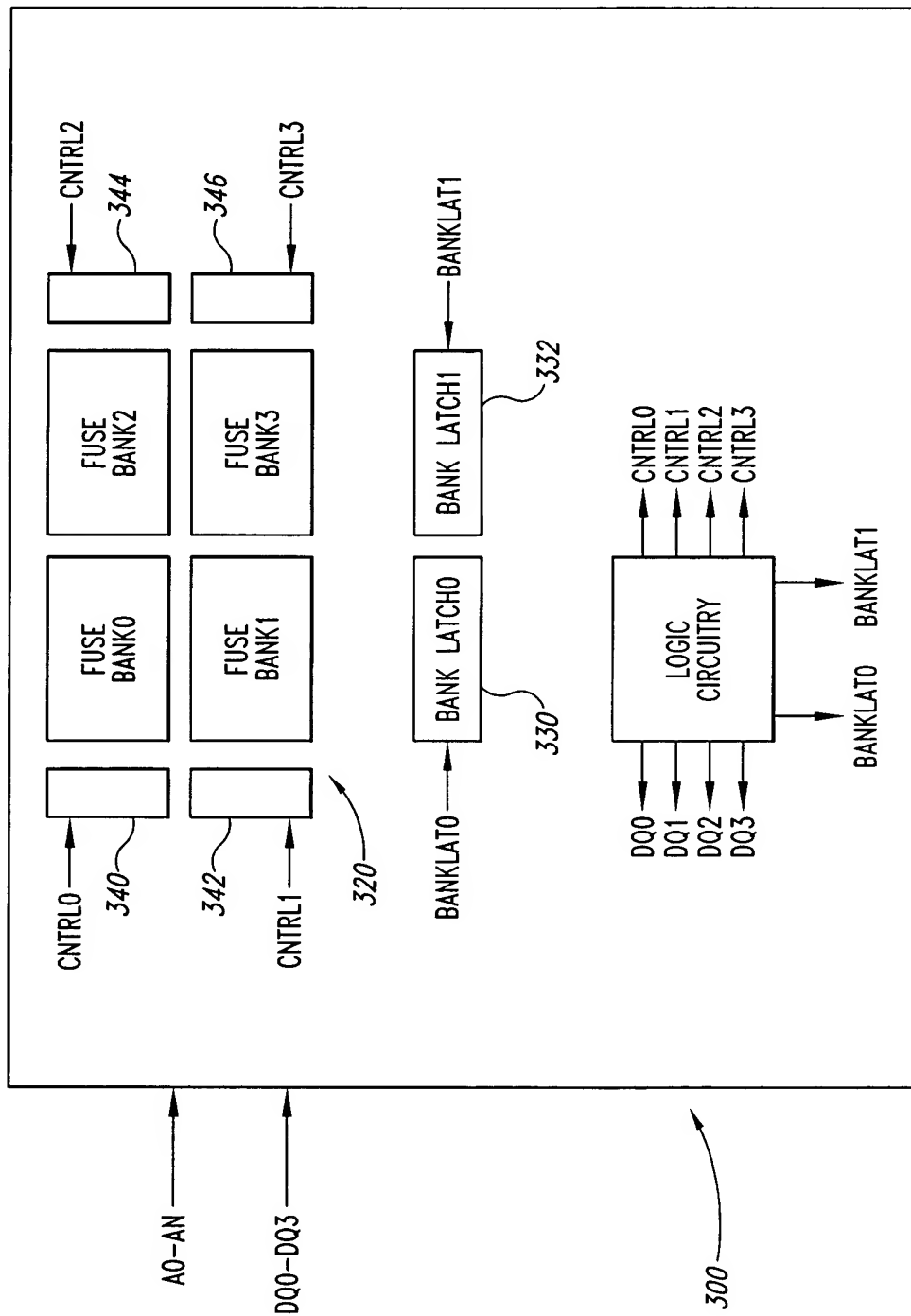


Fig. 2

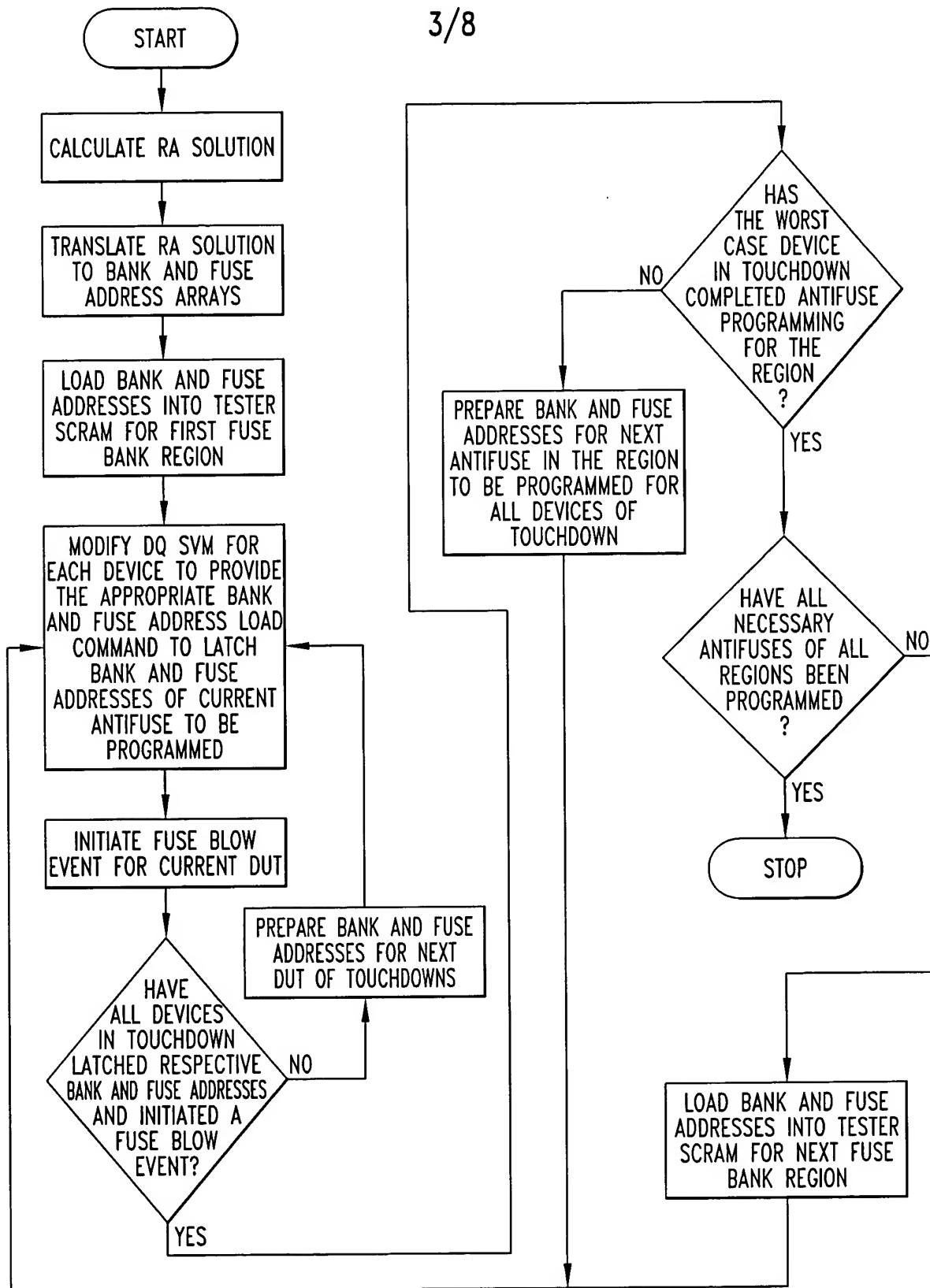
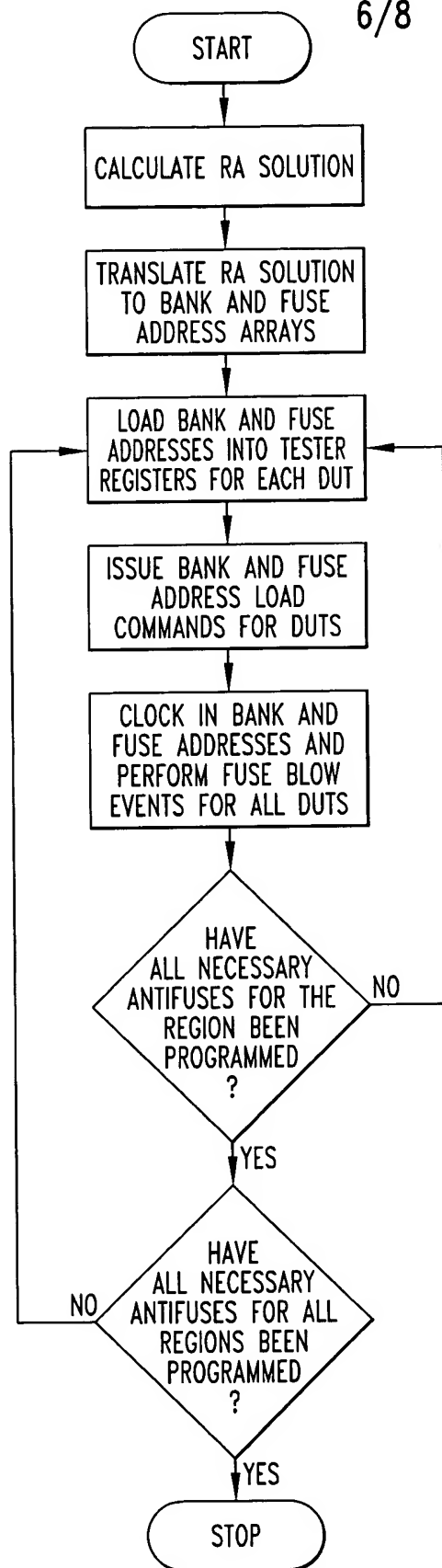
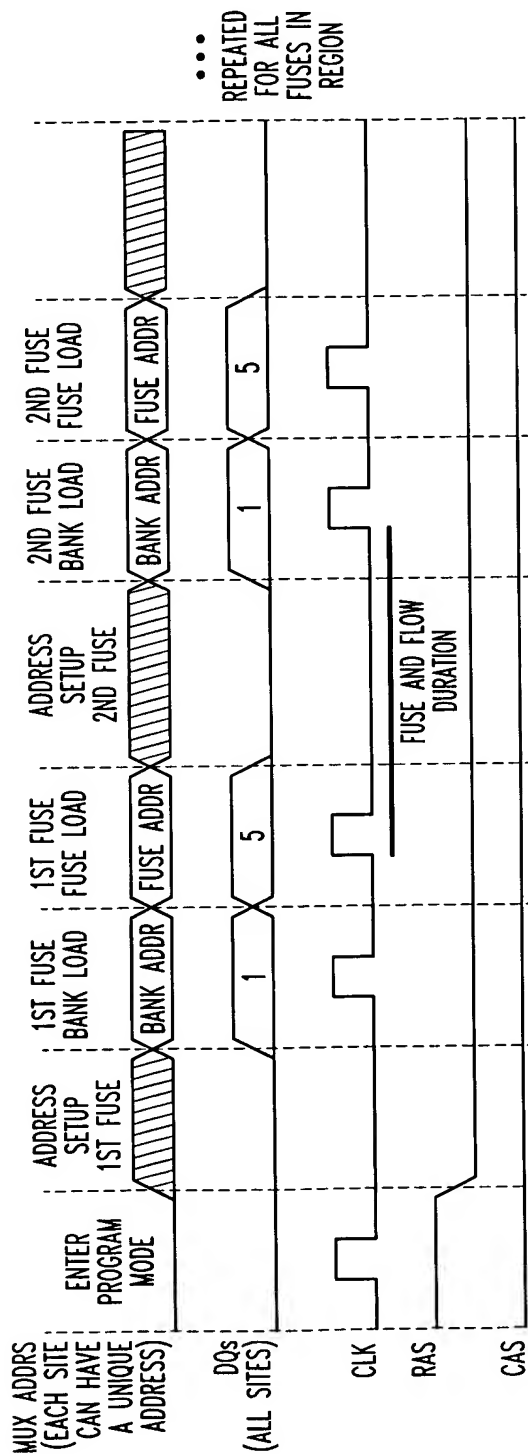


Fig. 3

*Fig. 6*



DQs FOR BANK AND FUSE ADDRESS LOADING

HEX	FUNCTION
0000	LOAD NOTHING
0001	LOAD BANK ADDRESS LEFT
0010	LOAD BANK ADDRESS RIGHT
0101	LOAD FUSE ADDRESS TOP LEFT
0110	LOAD FUSE ADDRESS TOP RIGHT
1001	LOAD FUSE ADDRESS BOTTOM LEFT
1010	LOAD FUSE ADDRESS BOTTOM RIGHT

DEVICE LAYOUT

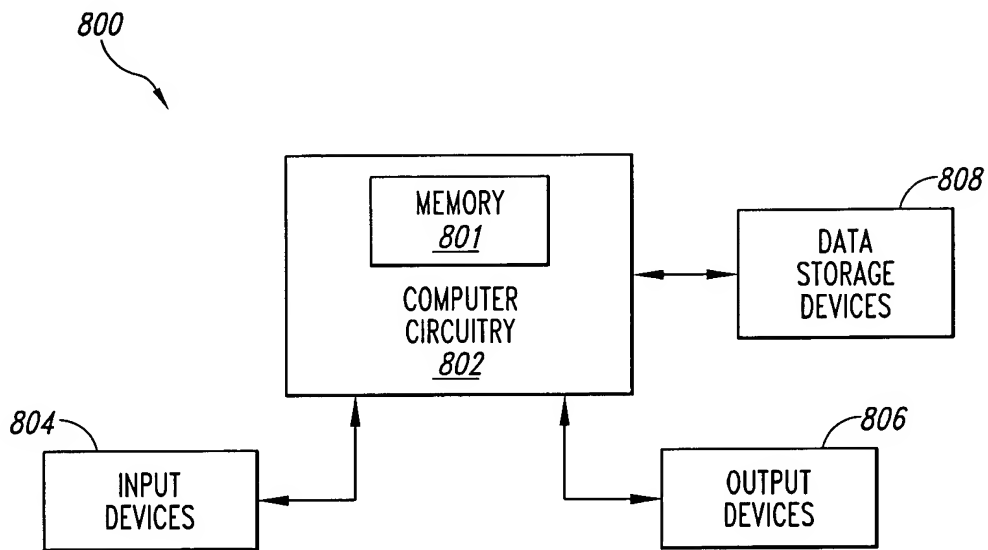
DEVICE BANK (FUSE REGION) 0			
DQs			
3210			
0001	LOAD BANK ADDRESS		
0101	LOAD FUSE ADDRESS		

DEVICE BANK (FUSE REGION) 1			
DQs			
3210			
0001	LOAD BANK ADDRESS		
1001	LOAD FUSE ADDRESS		

DEVICE BANK (FUSE REGION) 2			
DQs			
3210			
0010	LOAD BANK ADDRESS		
0110	LOAD FUSE ADDRESS		

DEVICE BANK (FUSE REGION) 3			
DQs			
3210			
0010	LOAD BANK ADDRESS		
1010	LOAD FUSE ADDRESS		

Fig. 7

*Fig. 8*